Interpretative Semantics for \texttt{prialt}-free
Handel-C

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1 Introduction

Handel-C [Emi]b is a language developed by the Hardware Compilation Group at Oxford’s Computer Laboratory. It is a hybrid of C and CSP [Hoc90], designed to target hardware implementations, specifically field-programmable gate arrays (FPGAs) [PL91, SP93, BHP94, LKL+95]. The language can be viewed as a pointer-free subset of C, augmented with a parallel construct and chan-
nel communication, as found in CSP. The type system has been modified to explicitly refer to the number of bits required to implement any given type. The language targets synchronous hardware with a single master clock. All assignments and channel communication events take one clock cycle, with all updates synchronised with the clock edge marking the cycle end. All expression and conditional evaluation (for selection, loops) is deemed to take 'zero-time', effectively being completed before the current clock cycle ends.

In order to facilitate work on a formal semantics for Handel-C, it was decided at first to consider a subset only, namely the smallest subset that would catch all the essential and potentially difficult aspects. After discussions with Ian Page, a suitable subset of Handel-C was identified. It includes channels, assignment, conditional, one loop, and the parallel construct as well as shared variables. Key omissions include types, macros, shared expressions, RAMs, ROMs, arrays and the bus interface material, all of which can be handled fairly straightforwardly, in semantic terms. Discussing requirements for a set of algebraic laws with Jim Woodcock, in particular with respect to normal forms, it was decided to include the 'prioritised alternatives' construct (prialt) in the reduced syntax.

Another key motivation for having the prialt is that it is the only mechanism for introducing non-blocking (or try-else-skip) synchronisations into programs.

This report presents an interpretive semantics of a prialt-free subset of the Handel-C hardware compilation language. The language is presented here with such a construct, but its semantics are not fully described at this point. This is due to the lack of clear documentation on the behaviour of this construct, which will require some experimentation in order to be able to elucidate an appropriate semantics. This has been left as a future task.

However, the semantics presented here has been defined with prialt in mind, which explains why the semantics of communication appears more complicated than it needs to be.

This document presents the semantics, but does not provide any formal or informal validation. This will be the subject of another technical report.

In §2 we introduce the abstract syntax, which we the augment with an explicit environment mapping in §3. Given these, in §4 we present the 'control-flow' or interpretive semantics.

# 2 Reduced Abstract Syntax

We present an abstract form of the reduced form of the Handel-C language, in the form of an abstract data type capturing the essential syntactical structure. This is based on chapter 9 of the Handel-C Language Reference Manual, v2.1 [Emb], as indicated in the section headings.

## 2.1 Program (§9.3.17, p197)

\[
\text{Prg} \ ::= \ \text{InitDecl}^* \times \text{Decl}^* \times \text{Stmt}
\]
2.2 Declarations (§9.3.6, p189)

Identifiers (Id) in this subset play three rôles, naming variables (Id_v), channels (Id_c) and (builtin) operators (Id_o). Even though these three uses are syntactically distinguishable, allowing us to treat them as separate name spaces, we prefer to keep one name-space for variables and channels, to avoid having to carry distinct mappings around.

\[ \text{InitDecl} :::= \text{Id} \times \mathbb{Z} \]

Local declarations have no initialisers.

\[ \text{Decl} :::= \text{VAR Id} \\
| \text{CHAN Id} \\
| \text{CHIN Id} \\
| \text{CHOUT Id} \]

2.3 Statements (§9.3.16, pp196–7)

In Handel-C we have the usual assignment (ASG), sequencing (SEQ), conditional (IF), looping (WHL) and jumping (BRK) constructs familiar to imperative language programmers. From CSP, we obtain the parallel construct (PAR) and channel communication primitives for input (IN) and output (OUT). Specific to Handel-C is the delay statement (DLY) which does nothing, but takes one full clock-cycle to do it.

\[ \text{Stmnt} :::= \text{SEQ Decl}\ast \text{Stmnt}\ast + \\
| \text{PAR Decl}\ast \text{Stmnt}\ast 2+ \\
| \text{ASG Id Expn} \\
| \text{IN Id Id} \\
| \text{OUT Id Expn} \\
| \text{IF Expn Stmnt [Stmnt]} \\
| \text{WHL Expn Stmnt} \\
| \text{PRI PCse}\ast \\
| \text{BRK} | \text{DLY} | \text{Cont} \]

The CONT (continue) statement is not in Handel-C, but is provided here as a technical device to facilitate translation into the explicit environment syntax form.

2.4 prialt Cases (§9.3.16, p197)

The prialt statement is a sequence of communication requests (CIN,COUT), each with an associated statement that is executed afterwards, if the corresponding communication succeeds. A prialt statement may have a final ‘default’ clause,
which always succeeds

\[
P_C s e := \begin{array}{c}
C I n \ Id \ Id \ S t m t \\
| \ C O u t \ Id \ E x p r n \ S t m n t \\
| \ D e f a u l t \ S t m t
\end{array}
\]

The ordering of the prior clauses is significant in that it assigns a priority to each request.

2.5 Expressions (§9.3.15, pp194–5)

Handel-C has the usual range of expressions, as well as ones tailored more closely to its hardware targeting rôle. However the details are not really of any immediate interest here. Instead we simply assume a simple expression grammar built up from numbers (\texttt{NUM}), variables (\texttt{VAR}) and applications (\texttt{APP}) of builtin operators and functions

\[
E x p r n := \begin{array}{c}
\texttt{NUM} \ Z \\
| \ \texttt{VAR} \ Id \\
| \ \texttt{APP} \ Id \ E x p r n^+
\end{array}
\]

2.6 Identifier (§9.3.1, p186)

Identifiers are just a countable type with equality defined, typically represented by character sequences

\[
I d := \mathbb{A}^+
\]
2.7 Abstract Syntax Summary

\[ Prg ::= InitDecl^* \times Decl^* \times Stmt \]

\[ InitDecl ::= Id \times \mathbb{Z} \]

\[ Decl ::= VAR Id \\
| \text{Chan} Id \\
| \text{ChIn} Id \\
| \text{ChOut} Id \]

\[ Stmt ::= \text{SEQ} Decl^* Stmt^+ \\
| \text{PAR} Decl^* Stmt^+ \\
| \text{ASG} Id \text{Exprn} \\
| \text{IN} Id Id \\
| \text{OUT} Id \text{Exprn} \\
| \text{IF} \text{Exprn} \text{Stmt} [\text{Stmt}] \\
| \text{WHI} \text{Exprn} \text{Stmt} \\
| \text{PRI} \text{PCse}^* \\
| \text{BRK} | \text{DLY} | \text{Cont} \]

\[ PCse ::= \text{CIN} Id_c Id_v \text{Stmt} \\
| \text{COUT} Id_c \text{Expr} \text{Stmt} \\
| \text{DEFAULT} \text{Stmt} \]

\[ Expr ::= \text{NUM} \mathbb{Z} \\
| \text{VAR} Id \\
| \text{APP} Id \text{Expr}^+ \]

\[ Id ::= \mathbb{A}^+ \]

3 Explicit Environment Abstract Syntax

As all program variables are statically allocated (there are no function/procedure call frames in Handel-C), we convert all declarations into an environment mapping, which maps tagged identifiers to their values. The tags are tree cursors (natural number sequences) that identify the point in the abstract syntax tree where the declaration occurred.
3.1 Trees

3.1.1 Tree Type

We shall assume all trees are expressed in the general form

$$(a, r), T \in \text{Tree } A \equiv A \times (\text{Tree } A)^*$$

3.1.2 Tree Access Functions

We have functions to access the node ($\mathcal{N}$), determine the number of sub-trees ($\#$) and to access the $i$th sub-tree ($\tau$):

$$\mathcal{N} : \text{Tree } A \to A$$
$$\mathcal{N}(a, r) \equiv a$$
$$\# : \text{Tree } A \to \mathbb{N}$$
$$\#(\tau) \equiv \text{len } \tau$$

$$\tau : (\text{Tree } A) \times \mathbb{N}_1 \to \text{Tree } A$$
$$\text{pre}(T, i) \equiv i \leq \# T$$

$(\tau)i \equiv \tau[i]$

3.2 Tree Cursors

3.2.1 Tree Cursor Type

A tree cursor ($TC$) is a sequence of non-zero natural numbers:

$$\kappa \in TC \equiv \mathbb{N}_1^*$$

A tree cursor denotes the path to be followed from the root node to the indicated node, by identifying number of each successive sub-tree to be followed. An empty sequence denotes the root, while a non-empty sequence gives the sub-tree indices in reverse order. For example, the sequence $(3, 5, 2)$ denotes the 3rd sub-tree of the 5th sub-tree of the 2nd sub-tree of the root. This reversal is done to make the operations of moving to a parent or child simpler, in that they can be directly expressed as head and tail operations.

3.2.2 Manipulating Tree Cursors

A Tree cursor can be modified to refer to the parent or the $i$th child of the tree node it denotes, provided, of course this is defined.

$$\uparrow \quad : \quad TC \to TC$$
$$\text{pre}(\kappa \uparrow) \equiv \kappa \neq A$$
$$\kappa \uparrow \equiv \text{tl } \kappa$$

$$\downarrow \quad : \quad TC \times \mathbb{N}_1 \to TC$$
$$\kappa \downarrow i \equiv i : \kappa$$
3.2.3 Tree Lookup

We can apply a cursor to a tree to find the relevant sub-tree node. This is only defined if the cursor matches the tree structure:

\[ \downarrow \downarrow \] : Tree A → Cursor → Tree A

\[ pre-T(A) \equiv \text{TRUE} \]

\[ pre-(\tau)(\kappa \sim \langle \hat{i} \rangle) \equiv i \leq \text{len } \tau \land pre-(\hat{\tau})(\kappa) \]

\[ T(A) \equiv T \]

\[ (\tau)(\kappa \sim \langle \hat{i} \rangle) \equiv \tau[\hat{i}](\kappa) \]

3.3 Tree Environments

3.3.1 Cursor-Tagged Identifiers

We tag identifiers by pairing them with a tree cursor denoting the tree node where the identifier was declared:

\[ CtlI \equiv TC \times Id \]

3.3.2 Tree Environment Type

A Tree Environment \((TEnv \ V)\) is a (finite) mapping from cursor tagged identifiers to values drawn from type \(V\):

\[ \rho \in TEnv \ V \equiv CtlI \rightarrow V \]

3.3.3 Tree Environment Lookup/Update

As we are recording all identifiers in the explicit syntax with cursor tags indicating their point of declaration, we can use simple map application and override to achieve lookup and update.

3.3.4 Tree Environment Key Lookup

We need to take an identifier tagged with its point of use, and look it up in the environment in order to re-tag it with its point of declaration:

\[ \text{key} : TEnv \ V \rightarrow CtlI \rightarrow CtlI \]

\[ \text{key}[\rho](\kappa, i) \equiv (\kappa, i) \in \text{dom } \rho \rightarrow (\kappa, i), \text{ key}[\rho](\kappa \uparrow, i) \]

The use of this function in the sequel depends on variables being declared before use, in the ordering resulting from an prefix traversal of the syntax tree.

3.4 Variable Data and Channel States

We need to define the data values and the channel states that our semantics will need. Ordinary variables simply have a data value, whereas channel variables
have channel states, as well as pointers to the reading and writing statements and channel data values

\[
\text{ValState} ::= \text{Val Data} \\
\text{CHAN ChState TC TC} \\
\text{INCh ChState TC} \\
\text{OUTCh ChState TC} \\
\text{STOP | GO}
\]

We will use null (root) tree cursors here to denote a null value, rather than the root. This makes sense because the tree root (entire program) can never be a channel statement. A channel’s state is either idle, or waiting for either input or output, or ready:

\[
\text{ChState} \equiv \text{IDLE | WAIT | INPUT | OUTPUT | READY | ERROR}
\]

Data values (Data) are currently integers, extended with an unknown value (?):

\[
\text{Data} \equiv \mathbb{Z} \cup \{?\}
\]

We shall define our program environment (Env) to be a ValState tagged environment:

\[
\text{Env} \equiv T\text{Env ValState}
\]

### 3.5 Explicit Environment Syntax

We are going to replace an abstract syntax based on Prg by one based on a new syntax tree based on a reworked definition of Stmt.

#### 3.5.1 Statements revisited

We discard any declarations (to be replaced by a single tree environment) and the sub-tree components, tag all identifiers, and introduce a continue and generalised delay statements:

\[
\text{Stmt} ::= \text{SEQ | PAR} \\
\text{ASG CtlId Expr} \\
\text{IN CtlId CtlId} \\
\text{OUT CtlId Expr} \\
\text{IF Expr} \\
\text{WH Expr} \\
\text{PRI} \\
\text{BRK | CONT} \\
\text{DLV N}
\]

#### 3.5.2 prialt Cases revisited

The prialt cases now become sequential statements whose first sub-statement is the synchronisation statement.
3.5.3 Expression revisited

We redefine expressions to use $\text{CtIds}$ instead of $\text{Ids}$:

$$\text{Expr} \quad ::= \quad \text{NUM} \ Z \quad | \quad \text{VAR} \ \text{CtId} \quad | \quad \text{APP} \ \text{CtId} \text{Expr}^+$$

We can define the evaluation of an expression in a given environment as

$$\begin{align*}
J(\_ & ) : \text{Expr} \to \text{Env} \to \text{Data} \\
\rho(\text{NUM} \ n) & \triangleq n \\
\rho(\text{VAR} \ v) & \triangleq n \ \text{where} \ (\text{VAL} n) = \rho(v) \\
\rho(\text{APP} \ f \ \sigma) & \triangleq [f] \circ \rho^* \sigma
\end{align*}$$

We have overloaded the map application notation. Which is meant is determined by the type of the operand: if an expression, it is what has just been defined, if a tagged identifier, it is regular map lookup. Here $[f]$ gives the fixed meaning of $f$ which is builtin.

3.5.4 Abstract Syntax Tree revisited

We then define our new abstract syntax as a tree over $\text{Stmt}$ with an invariant that restricts the length ($\ell = \#T$) of the sub-tree sequence:

$$\begin{align*}
\text{Stmt} & \triangleq \text{Tree} \ \text{Stmt} \\
\text{inv-Stmt} & \quad \text{SEQ} \quad \triangleq \quad \ell > 0 \\
\text{inv-Stmt} & \quad \text{PAR} \quad \triangleq \quad \ell > 1 \\
\text{inv-Stmt} & \quad \text{(If \_)} \quad \triangleq \quad \ell \in 1, 2 \\
\text{inv-Stmt} & \quad \text{(WHL \_)} \quad \triangleq \quad \ell = 1 \\
\text{inv-Stmt} & \quad \text{PRI} \quad \triangleq \quad \ell > 0 \\
\text{inv-Stmt} & \quad \text{T} \quad \triangleq \quad \ell = 0
\end{align*}$$

Not shown here is that fact that the invariant is applied recursively to all sub-trees.

3.5.5 Program revisited

Our new program representation is a new abstract syntax tree coupled with a tree environment:

$$\text{Prog} \quad \triangleq \quad \text{Stmt} \times \text{Env}$$

Note that a program is now any statement with an associated environment. Note also that strictly speaking we need an invariant that states that all cursors in the domain of the environment (as tags) must be compatible with the statement tree. By compatible, we mean that they refer to actual tree nodes, and being fully pedantic, that they only indicate $\text{SEQ}$ or $\text{PAR}$ nodes, as these are the only ones to have declarations in the original Handel-C.
3.6 Build the Explicit Environment Tree

We need to convert a program from $Prog$ to $Prog\_form$. We do this by creating an initial environment, and traversing the original tree with a cursor tracking our position, converting declarations to tagged environment entries, and building the new tree.

3.6.1 Converting $Prog$

We use the program’s declarations to build an initial environment, tagged with the null (root) cursor. The program main statement becomes the sole sub-tree of a (top-level) SEQ node:

$$\text{cvnPrg} : \quad Prg \rightarrow Prog$$
$$\text{cvnPrg}(\iota, \delta, s_0) \equiv \text{cvnStmt}(\iota, (1, \rho_0), s_0)$$
$$\text{where}\ \rho_0 = \text{cvnInitDecls}\ i \uplus \text{cvnDecls}[\Lambda\delta]$$

3.6.2 Converting $InitDecl$

Initial Declarations simply become mappings from root-tagged identifiers to their initial values:

$$\text{cvnInitDecl} : \quad InitDecl \rightarrow Env$$
$$\text{cvnInitDecl}(v, \kappa_0) \equiv \{(\Lambda, v) \mapsto (\text{val} \ \kappa_0)\}$$

$$\text{cvnInitDecls} : \quad InitDecl^* \rightarrow Env$$
$$\text{cvnInitDecls}\ \Lambda \equiv \theta$$
$$\text{cvnInitDecls}(i : \iota) \equiv (\text{cvnInitDecl}\ i) \uplus (\text{cvnInitDecls}\ \iota)$$

Note that later declarations supersede earlier ones. In Handel-C multiple declarations of the same identifier in the same scope are illegal, so this is not an issue. We simply take this approach using override to keep the semantics simple.

3.6.3 Converting $Decl$

$$\text{cvnDecl} : \quad TC \rightarrow Decl \rightarrow Env$$
$$\text{cvnDecl}(\kappa)(\text{VAR}'\ v) \equiv \{(\kappa, v) \mapsto \text{val} \ ?\}$$
$$\text{cvnDecl}(\kappa)(\text{CHAN}'\ c) \equiv \{(\kappa, c) \mapsto \text{CHAN Idle} \ \Lambda \ \Lambda\}$$
$$\text{cvnDecl}(\kappa)(\text{CHIN}'\ c) \equiv \{(\kappa, c) \mapsto \text{CHIN Idle} \ \Lambda\}$$
$$\text{cvnDecl}(\kappa)(\text{CHOUT}'\ c) \equiv \{(\kappa, c) \mapsto \text{CHOUT Idle} \ \Lambda\}$$

$$\text{cvnDecls} : \quad TC \rightarrow Decl^* \rightarrow Env$$
$$\text{cvnDecls}[\kappa]\ \Lambda \equiv \theta$$
$$\text{cvnDecls}[\kappa](d : \delta) \equiv (\text{cvnDecl}[\kappa]\ d) \uplus (\text{cvnDecls}[\kappa]\ \delta)$$
3.6.4 Converting Stmt

We convert a statement on a case-by-case basis:

\[
\text{convStmt} : \quad \text{TC} \times \text{Env} \rightarrow \text{Stmt} \rightarrow \text{StmtT} \times \text{Env}
\]

\[
\text{convStmt}[\kappa, \rho](\text{SEQ} \, \delta \, \sigma) \\
\quad \triangleq \; \text{let } \rho' = \rho \cup \text{convDecls}[\kappa] \delta \\
\quad \text{in let } (\tau, \rho'') = \text{convStmts}[\kappa, \rho'][\sigma] \text{ in } ((\text{SEQ}, \tau), \rho')
\]

\[
\text{convStmt}[\kappa, \rho](\text{PAR} \, \delta \, \sigma) \\
\quad \triangleq \; \text{let } \rho' = \rho \cup \text{convDecls}[\kappa] \delta \cup \{(\kappa, \tau) \mapsto \text{VAL} \, 0\} \\
\quad \text{in let } (\tau, \rho'') = \text{convStmts}[\kappa, \rho'][\sigma] \text{ in } ((\text{PAR}, \tau), \rho')
\]

\[
\text{convStmt}[\kappa, \rho](\text{ASM} \, v \, e) \\
\quad \triangleq \; ((\text{ASM} \, (\text{key}[\rho](\kappa, v)) \, e, \Lambda), \rho)
\]

\[
\text{convStmt}[\kappa, \rho](\text{IN} \, c \, e) \\
\quad \triangleq \; ((\text{IN} \, (\text{key}[\rho](\kappa, c)) \, (\text{key}[\rho](\kappa, v)) \, e, \Lambda), \rho)
\]

\[
\text{convStmt}[\kappa, \rho](\text{OUT} \, c \, e) \\
\quad \triangleq \; ((\text{OUT} \, (\text{key}[\rho](\kappa, c)) \, e, \Lambda), \rho)
\]

\[
\text{convStmt}[\kappa, \rho](\text{IF} \, e \, s_1 \, s_2) \\
\quad \triangleq \; \text{let } (T_1, \rho_1) = \text{convStmt}[\kappa, \rho](1, s_1) \\
\quad \text{in let } (T_2, \rho_2) = \text{convStmt}[\kappa, \rho](2, s_2) \text{ in } ((\text{IF} \, e, \langle T_1, T_2 \rangle), \rho_1 \cup \rho_2)
\]

\[
\text{convStmt}[\kappa, \rho](\text{WHILE} \, e \, s) \\
\quad \triangleq \; \text{let } (T', \rho') = \text{convStmt}[\kappa, \rho](1, s) \text{ in } ((\text{WHILE} \, e, \langle T' \rangle), \rho')
\]

\[
\text{convStmt}[\kappa, \rho](\text{PRI} \, \varnothing) \\
\quad \triangleq \; \text{let } (\tau', \rho') = \text{convStmts}[\kappa, \rho](\text{convPCase}[\kappa] \ast \varnothing) \\
\quad \text{in } ((\text{PRI} \, \tau'), \rho' \cup \{(\kappa, \varnothing) \mapsto \text{STOP}\})
\]

\[
\text{convStmt}[\kappa, \rho](\text{BRK}) \\
\quad \triangleq \; ((\text{BRK}, \Lambda), \rho)
\]

\[
\text{convStmt}[\kappa, \rho](\text{DLY}) \\
\quad \triangleq \; ((\text{DLY} \, 1, \Lambda), \rho)
\]

\[
\text{convStmt}[\kappa, \rho](\text{CONT}) \\
\quad \triangleq \; ((\text{CONT}, \Lambda), \rho)
\]

We provide a form for converting the \textit{i}th sub-statement directly — this is used above, as well as below for handling sequences of sub-statements:

\[
\text{convStmt} : \quad \text{TC} \times \text{Env} \rightarrow \mathbb{N}_1 \times \text{Stmt} \rightarrow \text{StmtT} \times \text{Env}
\]

\[
\text{convStmt}[\kappa, \rho](i, s) \triangleq \text{convStmt}[i : \kappa, \rho] s
\]

Converting statement sequences simply involves generating appropriate indices and then zipping these with the statements, mapping indexed conversion across
and unzipping to get the result:

\[
\text{cnvStmts} : \quad TC \times Env \rightarrow Stmt^+ \rightarrow StmtT^+ \times Env
\]

\[
\text{cnvStmts}[\kappa, \rho][\sigma] \triangleq (\tau, \rho')
\]

where

\[
\begin{align*}
\tau &= \langle 1 \ldots \text{len } \sigma \rangle \\
(\tau, \varrho) &= (\text{unzip } \circ (\text{cnvStmt}[\kappa, \rho]^* \circ \text{zip})(\tau, \sigma)) \\
\rho' &= \langle \rangle
\end{align*}
\]

### 3.6.5 Converting PCase

Converting `prialt` cases mainly involves stripping out the statements and converting the channel information to statements and returning those contained in a sequential compound statement.

\[
\text{cnvPCase} : \quad TC \rightarrow PCse \rightarrow Stmt
\]

\[
\begin{align*}
\text{cnvPCase}[\kappa](\text{CIN } c \ v \ s) &\triangleq \text{SEQ } \langle \text{IN (key}[\rho](\kappa, c)) (\text{key}[\rho](\kappa, v)) , s \rangle \\
\text{cnvPCase}[\kappa](\text{OUT } c \ e \ s) &\triangleq \text{SEQ } \langle \text{OUT (key}[\rho](\kappa, c)) e , s \rangle \\
\text{cnvPCase}[\kappa](\text{DEFAULT } s) &\triangleq \text{SEQ } \langle \text{CONT } , s \rangle
\end{align*}
\]
3.7 Explicit Environment Syntax Summary

\[ \text{Prog} \triangleq \text{Stmt} \times \text{Env} \]

\[ \text{Stmt} \triangleq \text{Tree Stmt} \]

\[ \text{inv-Stmt} \text{ SEQ} \triangleq \ell > 0 \]

\[ \text{inv-Stmt} \text{ PAR} \triangleq \ell > 1 \]

\[ \text{inv-Stmt} (\text{IF } \_ \_) \triangleq \ell \in 0,1 \]

\[ \text{inv-Stmt} (\text{WHL } \_ \_) \triangleq \ell = 1 \]

\[ \text{inv-Stmt} \text{ PRI} \triangleq \ell > 0 \]

\[ \text{inv-Stmt} \text{ T} \triangleq \ell = 0 \]

\[ \text{Stmt} ::= \text{SEQ} | \text{PAR} \]

\[ \quad | \text{ASG CtId Expr} \]

\[ \quad | \text{IN CtId CtId} \]

\[ \quad | \text{OUT CtId Expr} \]

\[ \quad | \text{IF Expr} \]

\[ \quad | \text{WHL Expr} \]

\[ \quad | \text{PRI} \]

\[ \quad | \text{BRK} | \text{CONT} \]

\[ \quad | \text{Dly N} \]

\[ \text{Expr} ::= \text{NUM Z} \]

\[ \quad | \text{VAR CtId} \]

\[ \quad | \text{APP CtId Expr}^+ \]

\[ \text{Id} ::= \mathbb{A}^+ \]

\[ \text{Env} \triangleq \text{CtId} \rightarrow \text{ValState} \]

\[ \text{ValState} ::= \text{VAL Data} \]

\[ \quad | \text{CHAN ChState TC TC} \]

\[ \quad | \text{INCH ChState TC} \]

\[ \quad | \text{OUTCH ChState TC} \]

\[ \quad | \text{STOP} | \text{GO} \]

\[ \text{Data} \triangleq \mathbb{Z} \cup \{?\} \]

\[ \text{ChState} \triangleq \text{IDLE} | \text{WAIT} | \text{INPUT} | \text{OUTPUT} | \text{READY} | \text{ERROR} \]
4 Control Flow (Interpretative) Semantics

4.1 Introduction

We shall model control flow by the notion of "program counters" or "execution pointers" which are simply tree cursors. In general, because of the parallelism, we will have a set of active execution pointers. If this set becomes empty, then the program has terminated. Program execution involves three conceptual phases:

- Execution Pointer Setup The execution pointers are updated to point to the next set of atomic statements to be executed. This phase starts and ends with pointers pointing only at atomic statements. Pointers may indicate composite statements during this process. This phase is in fact split into two sub-phases. The partial and communication statements require global knowledge to be fully handled, so the second phase, is used to finalise things once all the other active atomic statements have been identified.

- Right-hand Side evaluation The right-hand side expressions of all the selected assignments and channel statements are evaluated.

- Left-hand Side Assignment The left-hand variables and channel contents are then simultaneously updated.

These three phases comprise one clock cycle. The third phase coincides with the assignment clock edge.

4.2 Running the Program

We take a program, termination predicate and (initial) environment, determine the first execution pointers and then repeatedly cycle, until there are either no more execution pointers, or the termination predicate returns true. The program state is the current set of execution pointers and the environment:

$$\Sigma, (K, \rho) \in PState = PTC \times Env$$

The termination predicate is over program state:

$$\mathcal{U} \in TermPred \triangleq PState \rightarrow B$$

We return the program state at the time of termination.

$$\text{cfBeh} : TermPred \rightarrow PState$$

$$\text{cfBehStart}_{\mathcal{U}}(T, \rho_0) = \text{repeat cycle (fstep}_{T}(\rho) \Lambda \mathcal{U})$$

where

$$\mathcal{U}'(K, \rho) \triangleq \mathcal{U}(K, \rho) \cup K = \emptyset$$

The most useful purpose for the predicate is to allow either 'single-stepping' or stopping at external (observable) communications events. Such a stopped
The program can be restarted by:

\[
\text{cfBehResume} : \quad \text{TermPred} \rightarrow \text{StmtT} \rightarrow \text{PState} \rightarrow \text{PState}
\]

\[
\text{pre-cfBehResume}_T[K, \rho] \triangleq K \neq \emptyset
\]

\[
\text{cfBehResume}_T[K, \rho] \triangleq \text{repeat cycle}(K, \rho) \cup'
\]

where

\[
\cup'(K, \rho) \triangleq \cup(K, \rho) \cup K = \emptyset
\]

The precondition is not strictly necessary as \text{cycle} acts like an identity function if \(K\) is empty.

4.3 The Clock Cycle

We take the program, environment and set of execution pointers and return a modified environment and set of next execution pointers. We carry out the three phases in the order: evaluate rhs, assign lhs, and determine next execution pointers:

\[
\text{cycle} : \quad \text{StmtT} \rightarrow \text{PState} \rightarrow \text{PState}
\]

\[
\text{cycle}_T(K, \rho) \triangleq \begin{aligned}
\text{let } & \rho_r = \text{evalRhs}_{T, \rho}[\theta]K \text{ in} \\
\text{let } & \rho_c = \text{evalComm}_{T, \rho}[\theta]K \text{ in} \\
\text{let } & \rho_t = \rho_r \uparrow (\rho_r \cup \rho_c) \text{ in} \\
\text{incExecPtr}_{T}(K, \rho_t)
\end{aligned}
\]

The reason for this ordering is that we call \text{cycle} with an initial set of execution pointers.

4.3.1 Evaluating the RHS

For assignments we simply evaluate the rhs in the current environment and set the variable to that value in the new environment:

\[
\text{evalRhs} : \quad \text{StmtT} \times \text{Env} \rightarrow \text{Env} \rightarrow \text{PTC} \rightarrow \text{Env}
\]

\[
\text{evalRhs}_{T, \rho}[\rho'][\theta] \emptyset \triangleq \rho'
\]

\[
\text{evalRhs}_{T, \rho}[\rho'](K \cup \{ \kappa \}) \triangleq \text{evalRhs}_{T, \rho}[\rho' \uparrow \rho']K
\]

where

\[
\rho'' \triangleq T(\kappa) = (\text{ASG} v e) \rightarrow \{ v \mapsto \text{VAL} \rho(e) \}, \theta
\]

For communication statements we simply evaluate the output rhs in the current environment and set the input variable to that value in the new environment. We work through the environment, rather than the execution pointers because
we need to pick up both statements for each channel:

evalComms
\[
\begin{align*}
\text{evalComms} & : \ PntT \times \text{Env} \to \text{Env} \\
\text{evalComms}_T, \rho[\rho'][\theta] & \define \rho' \\
\text{evalComms}_T, \rho[\rho'][\mu \cup \{c \mapsto s\}] & \define \text{evalComms}_T, \rho[\rho' \uplus \rho''] \mu \\
\text{where} & \\
\rho'' & \define \text{stateOf}(\rho(c) = \text{READY} \to \text{evalComms}[T, \rho](\rho(c)), \theta)
\end{align*}
\]

evalComm
\[
\begin{align*}
\text{evalComm} & : \ PntT \times \text{Env} \to \text{ValStateEnv} \\
\text{evalComm}[T, \rho](\text{CHAN} \ \text{READY} \ \kappa_i, \kappa_o) & \define \text{let} \ (\text{OUT} \ e) = \pi_1(T(\kappa_o)) \\
& \text{and} \ (\text{IN} \ e) = \pi_1(T(\kappa_i)) \text{ in} \\
& \rho \uplus \{v \mapsto \text{VAL}(\rho(e))\}
\end{align*}
\]

4.3.2 ‘Incrementing’ Execution Pointers

We apply \text{nextp} to all existing pointers to get new ones, factor out the communication pointers, use \text{resep} to resolve them, and merge the outcome back in:

\[
\begin{align*}
\text{incExePtrs} & : \ PntT \to \text{PState} \to \text{PState} \\
\text{incExePtrs}_T(K, \rho) & \define \text{let} \ (K', \rho') = \text{foldNxt}_T(\emptyset, \rho')K' \text{ in} \\
& \text{let} (K_a, K_p) = (\text{Pr} [P_T K'], \text{Pr} [P_T K']) \text{ in} \\
& \text{let}(K', \rho'') = \text{resep}_T(\rho')K_p \text{ in} \\
& (K_a \cup K_v, \rho'') \\
\text{where} & \\
P_T(\kappa) & \define \pi_1(T(\kappa)) = \text{Pr} \\
\vee \ & \pi_1(T(\kappa)) = (\text{IN} \ e) \\
\vee \ & \pi_1(T(\kappa)) = (\text{OUT} \ e)
\end{align*}
\]

This function simply applies \text{nextp} to a set of execution pointers, passing the environment between them:

\[
\begin{align*}
\text{foldNxt} & : \ PntT \to \text{PState} \to \text{PTC} \to \text{PState} \\
\text{foldNxt}_T(K, \rho) & \define (K, \rho) \\
\text{foldNxt}_T(K_a, \rho)(K_p \cup \{\kappa\}) & \define \text{foldNxt}_T(K_a \cup K', \rho')K_p \\
\text{where} & \\
(K', \rho') & \define \text{nextp}_T(\rho)\kappa
\end{align*}
\]

This function is defined recursively over sets, so it is potentially non-deterministic. We need to show it actually is a function. The key to such a proof is that multiple execution pointers only result for \text{PAR} statements, and their sole interaction

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is to increment and decrement the thread count for the PAR statement (§4.5.4), in a manner that is independent of the order in which each thread is created or destroyed.

4.4 Determining Next Execution Pointers

The process of computing the next execution pointers involves the following functions:

- **fstep (first execution point)** — given an execution pointer pointing to a statement, this identifies the first atomic statements to be executed in that statement, relative to the current state of the environment. In general it may return zero, one, or more pointers, as well as modifying the environment (or that portion of it which deals with synchronisation). For PIC statements, it returns a pointer to the statement itself, and not an atomic sub-statement.

- **nxtstep (next execution point)** — given an execution pointer pointing to a statement, this seeks to identify the next atomic statement to be executed, by looking at the parent of the current statement. As above, it will refer to the current environment, and it may return zero, one, or more pointers, and a modified environment.

- **resep (resolve communication execution points)** — given execution pointer for all active communication statements, it determines, by looking at the state of the channels involved, which are going to execute.

The function **fstep** is applied to the program (root statement) at the beginning to establish the first execution points. The first phase simply applies **nxtstep** to every current execution point, then applies **resep** to all selected communication statements, and combines the results.

4.5 Definitions of fstep and nxtstep

We shall start by giving a general overview of the function signatures and their first activity at the top-level. Then we present the behaviour for each statement type on a case by case basis.

Both functions take a program tree, environment, and execution pointer as arguments. Both return a set of execution pointers and an environment as results:

\[
\text{fstep} : \text{StmtT} \rightarrow \text{Env} \rightarrow \text{TC} \rightarrow \text{PState}
\]

\[
\text{nxtstep} : \text{StmtT} \rightarrow \text{Env} \rightarrow \text{TC} \rightarrow \text{PState}
\]

The program tree (T) is a fixed parameter, designating the entire program, and does not change.

The **fstep** function uses the execution pointer to lookup the current statement for case analysis. If the statement is atomic or a priori then we are done, and we
return that execution pointer. With the prialt we also set it’s status to STOP

\[
\text{fstep}_T(\rho)\kappa \equiv \text{case } T(\kappa)
\]  
\[
\quad (\text{ASG } \lambda_\kappa) \rightarrow ({\{\kappa}\}, \rho) \\
\quad (\text{DLY }, \lambda) \rightarrow ({\{\kappa}\}, \rho) \\
\quad (\text{IN } \lambda_\kappa) \rightarrow ({\{\kappa}\}, \rho) \\
\quad (\text{OUT } \lambda_\kappa) \rightarrow ({\{\kappa}\}, \rho) \\
\quad (\text{PRi }, \lambda) \rightarrow ({\{\kappa}\}, \rho \uplus ({\{\kappa}, \mathfrak{S}}) \mapsto \text{STOP})
\]

The other statement types will be dealt with below on a case-by-case basis. We shall use the form

\[
\text{fstep}_T(\rho)\kappa \text{ where } T(\kappa) = \ldots
\]

to denote the cases concerned.

The \text{nxtpe} function uses part of the execution pointer to lookup the current statement’s parent for case analysis, except for communications statements, discussed shortly. If the execution pointer is empty, then we are asking for the next statement after the program as a whole, so we respond with an empty pointer set, signalling program termination:

\[
\text{nxtpe}_T(\rho)\Lambda \triangleq (\emptyset, \rho)
\]

Otherwise, we determine if we have a communication statement that is blocked, in which case we return their execution pointers. Otherwise, we do case analysis on the parent, which must therefore be composite:

\[
\text{nxtpe}_T(\rho)(i : \kappa) \triangleq \text{case } T(i : \kappa)
\]
\[
\quad (\text{IN } c. v, \Lambda) \mid \text{stateOf}(\rho(i : \kappa, c)) = \text{INPUT} \\
\quad \rightarrow ({\{i\}, \kappa}, \rho) \\
\quad (\text{OUT } c. e, \Lambda) \mid \text{stateOf}(\rho(i : \kappa, c)) = \text{OUTPUT} \\
\quad \rightarrow ({\{i\}, \kappa}, \rho) \\
\quad (\text{PRi }, \sigma) \mid \rho(i : \kappa, \mathfrak{S}) = \text{STOP} \\
\quad \rightarrow ({\{i\}, \kappa}, \rho)
\]

The function stateOf is:

\[
\text{stateOf} : \quad \text{ValState} \rightarrow \text{ChState}
\]
\[
\text{stateOf(CHAN } s \_\) \triangleq s \\
\text{stateOf(INCH } s \_\) \triangleq s \\
\text{stateOf(OUTCH } s \_\) \triangleq s
\]
The remaining statement types will be dealt with below on a case-by-case basis. We shall use the form
\[ \text{next}_T(p)\(i : \kappa\) \text{ where } T(\kappa) = \ldots \]
to denote the cases concerned.

All the cases involve the composite statements. In general we assume that sub-statements are numbered 1 to \(N\) where \(N\) is the length of their sequence \((N = \text{len } \sigma)\).

### 4.5.1 The Seq Statement

The first execution point of a sequential statement is that of its first substatement:
\[
\text{fstep}_T(p)\kappa \text{ where } T(\kappa) = (\text{SEQ}, \langle s_1, \ldots, s_N \rangle) \\
\cong \text{fstep}_T(p)(1 : \kappa)
\]
Pictorially, we go from
\[
\text{fstep} \Rightarrow \kappa :: \text{SEQ} \\
(1 : \kappa) :: s_1 \ldots (i : \kappa) :: s_i \ldots \quad (N : \kappa) :: s_N
\]
to
\[
\kappa :: \text{SEQ} \\
\text{fstep} \Rightarrow (1 : \kappa) :: s_1 \ldots (i : \kappa) :: s_i \ldots \quad (N : \kappa) :: s_N
\]

The next execution point of a child of a sequence statement is the first of the next child in sequence, if any more remain. If we are starting from the last child then we look to the sequence’s own parent:
\[
\text{next}_T(p)(i : \kappa) \text{ where } T(\kappa) = (\text{SEQ}, \langle s_1, \ldots, s_N \rangle) \\
\cong i < N \rightarrow \text{fstep}_T(p)(i + 1 : \kappa) \\
i = N \rightarrow \text{next}_T(p)\kappa
\]

For all but the last child we go from
\[
(1 : \kappa) :: s_1 \ldots \quad \text{next} \Rightarrow (i : \kappa) :: s_i \quad (i + 1 : \kappa) :: s_{i+1} \ldots \quad (N : \kappa) :: s_N
\]
to
\[
\kappa :: \text{SEQ} \\
(1 : \kappa) :: s_1 \ldots \quad (i : \kappa) :: s_i \quad \text{fstep} \Rightarrow (i + 1 : \kappa) :: s_{i+1} \ldots \quad (N : \kappa) :: s_N
\]
For the last child we go from

\[
\kappa :: \text{SEQ} \\
(1:\kappa) :: s_1 \quad \ldots (i:\kappa) :: s_i \ldots \quad \text{nxt}\text{e}\text{p} \Rightarrow (N:\kappa) :: s_N
\]

to

\[
\text{nxt}\text{e}\text{p} \Rightarrow \kappa :: \text{SEQ} \\
(1:\kappa) :: s_1 \quad \ldots (i:\kappa) :: s_i \ldots \quad (N:\kappa) :: s_N
\]

### 4.5.2 The If Statement

The first execution point of an if-statement depends on the current value of the condition and is the first execution point of the appropriate sub-statement:

\[
\text{fstep}_T(p)\kappa \quad \text{where} \quad T(\kappa) = (\text{If } c, (s_l \ s_e))
\]

\[
\equiv \rho(c) \rightarrow \text{fstep}_T(p)(1:\kappa) \\
\neg\rho(c) \rightarrow \text{fstep}_T(p)(2:\kappa)
\]

Pictorially, we go from

\[
\text{fstep} \Rightarrow \kappa :: \text{If TRUE} \\
(1:\kappa) :: s_l \quad \quad (2:\kappa) :: s_e
\]

to

\[
\kappa :: \text{If TRUE} \\
\text{fstep} \Rightarrow (1:\kappa) :: s_l \quad \quad (2:\kappa) :: s_e
\]

and from

\[
\text{fstep} \Rightarrow \kappa :: \text{If FALSE} \\
(1:\kappa) :: s_l \quad \quad (2:\kappa) :: s_e
\]

to

\[
\kappa :: \text{If FALSE} \\
(1:\kappa) :: s_l \quad \quad \text{fstep} \Rightarrow (2:\kappa) :: s_e
\]
The first execution point of an if-statement with no else-statement depends on the current value of the condition and is the first execution point of the then-statement if true, otherwise we look for the next statement after the if:

\[
\text{fstep}_T(\rho) \kappa \quad \text{where} \quad T(\kappa) = (\text{If } c, (s_t)) \\
\equiv \quad \begin{cases} 
\rho(c) \rightarrow \text{fstep}_T(\rho)(1 : \kappa) \\
-\rho(c) \rightarrow \text{nxtstep}_T(\rho)\kappa
\end{cases}
\]

Pictorially, we go from

\[
fstep \Rightarrow \kappa :: \text{If True} \\
\quad \downarrow \\
(1 : \kappa) :: s_t
\]

to

\[
\kappa :: \text{If True} \\
\quad \downarrow \\
fstep \Rightarrow (1 : \kappa) :: s_t
\]

and from

\[
fstep \Rightarrow \kappa :: \text{If False} \\
\quad \downarrow \\
(1 : \kappa) :: s_t
\]

to

\[
nxtstep \Rightarrow \kappa :: \text{If False} \\
\quad \downarrow \\
(1 : \kappa) :: s_t
\]

The next execution point of a child of an if-statement is the next point of the parent itself, (regardless of the presence/absence of the else statement):

\[
nxtstep_T(\rho)(i : \kappa) \quad \text{where} \quad T(\kappa) = (\text{If } c, \sigma) \\
\equiv \quad \text{nxtstep}_T(\rho)\kappa
\]

We go from

\[
\kappa :: \text{If } c \\
\quad (1 : \kappa) :: s_t \quad \sim \text{nxtstep} \\
\quad \quad \Rightarrow ([2 : \kappa) :: s_e]
\]

to

\[
nxtstep \Rightarrow \kappa :: \text{If } c \\
\quad (1 : \kappa) :: s_t \\
\quad \quad [([2 : \kappa) :: s_e]
\]
4.5.3 The Whl Statement

The first execution point of an while-statement depends on the current value of the condition. If true, it is the first execution point of the body. If false, it is the next execution point after the while-statement.

\[
\text{fstep}_T(\rho) \kappa \quad \text{where} \quad T(\kappa) = (\text{Whl}, c, (s_b)) \\
\equiv \rho(c) \rightarrow \text{fstep}_T(\rho)(1 : \kappa) \\
-\rho(c) \rightarrow \text{nxtstep}_T(\rho)\kappa
\]

Pictorially, we go from

\[
\text{fstep} \Rightarrow \kappa :: \text{Whl True} \\
(1 : \kappa) :: s_b
\]

to

\[
\kappa :: \text{Whl True} \\
\text{fstep} \Rightarrow (1 : \kappa) :: s_b
\]

and from

\[
\text{fstep} \Rightarrow \kappa :: \text{Whl False} \\
(1 : \kappa) :: s_b
\]

to

\[
\text{nxtstep} \Rightarrow \kappa :: \text{Whl False} \\
(1 : \kappa) :: s_b
\]

The next execution point of the child of an while-statement is the first execution point of that child if the condition is true, otherwise it is the next execution point of the parent itself:

\[
\text{nxtstep}_T(\rho)(1 : \kappa) \quad \text{where} \quad T(\kappa) = (\text{Whl}, c, (s_b)) \\
\equiv \rho[c] \rightarrow \text{fstep}_T(\rho)(1 : \kappa) \\
-\rho[c] \rightarrow \text{nxtstep}_T(\rho)\kappa
\]

We go from

\[
\kappa :: \text{Whl True} \\
\text{nxtstep} \Rightarrow (1 : \kappa) :: s_b
\]
to

\[ \kappa :: \text{WHL True} \]

\[ \text{fstep} \Rightarrow (1 : \kappa) :: s_b \]

and from

\[ \kappa :: \text{WHL False} \]

\[ \text{nxttep} \Rightarrow (1 : \kappa) :: s_b \]

to

\[ \text{nxttep} \Rightarrow \kappa :: \text{WHL True} \]

\[ (1 : \kappa) :: s_b \]

The semantics for the WHL statement displays potentially divergent behaviour. If we have a fragment of syntax of the form (annotated with cursors):

\[ \kappa :: (\text{WHL True}, (1 : \kappa :: (\text{WHL False}, \sigma))) \]

and we try to compute \( \text{ftsep}_T(\rho)\kappa \) we obtain:

\[ \text{ftsep}_T(\rho)\kappa \]

\[ = \langle \text{Outer WHL condition is True} \rangle \]

\[ \text{ftsep}_T(\rho)(1 : \kappa) \]

\[ = \langle \text{Inner WHL condition is False} \rangle \]

\[ \text{nxttep}_T(\rho)(1 : \kappa) \]

\[ = \langle \text{Outer WHL condition is True} \rangle \]

\[ \text{fstep}_T(\rho)(1 : \kappa) \]

\[ = \langle \text{Inner WHL condition is False} \rangle \]

\[ \text{nxttep}_T(\rho)(1 : \kappa) \]

\[ : \]

We get an infinite regress! This is not a disaster, as we have captured an aspect of Handel-C’s own behaviour. Handel-C cannot generate the combinatorial logic to compute the next statement in this case. It reports a “combinatorial cycle” at this point. Handel-C issues this as a warning, and then fixes the problem by putting a DLY statement in parallel with the inner while loop. This breaks the combinatorial cycle, and allows the fstep and nxttep functions to terminate.

4.5.4 The Par Statement

The parallel statement is quite complex because (i) we produce multiple threads of execution, each with its own execution pointer, and (ii) we need to keep track of which threads have terminated, as the parallel construct terminates only when all the threads have done so. We use the environment, indexed by a special identifier (\( x \)) tagged with the execution pointer for the parallel construct itself,
to store the number of outstanding threads. As control passes into the \textsc{Par},
the relevant call to \texttt{fstep} updates the environment to reflect the fact that \( N \)
threads have been started. As each thread terminates, this value is decremented
by the corresponding call to \texttt{next}. This is the reason that these functions need
to return an environment value.

A technical difficulty could arise here. Some of the sub-statements in the parallel
construct could be while-loops with false guards. These will not run and will
terminate immediately, so decrementing the thread count. Indeed, if all the
bodies are such while-loops, the count will be set to \( N \), then decremented \( N \)
times as the calls to \texttt{fstep} turn into calls to \texttt{next}, finally resulting in \texttt{next}
being called on the \textsc{Par} itself! The key point is that the environment needs to
be chained among the \( N \) calls to \texttt{fstep} for each thread. We cannot simply do
the calls 'in parallel'.

We introduce an auxiliary function \texttt{fstpar} which applies \texttt{fstep} to a sequence
of statements, and manages the environment threading. The sequence is simply
denoted by the number of statements remaining to be processed:

\[
\texttt{fstpar} : \quad \text{StmtT} \to \text{PState} \to \text{TC} \to \mathbb{N} \to \mathcal{P} \text{TC} \times \text{Env}
\]

\[
\texttt{fstpar}_T(P, \rho)(\kappa 0) \equiv (P, \rho) \quad \texttt{fstpar}_T(P, \rho)(\kappa (i + 1)) \equiv \text{let } (P', \rho') = \texttt{fstep}_T(\rho)(i + 1 : \kappa) \text{ in } \texttt{fstpar}_T(P \cup P', \rho') \kappa i
\]

A property we need to check is that the interference between the \( N \) applications
of \texttt{fstep} is well-behaved.

The first execution point of a parallel statement is that of all its sub-statements:

\[
\texttt{fstep}_T(\rho)(\kappa \quad \text{where } T(\kappa) = (\text{Par}, \langle s_1, \ldots, s_N \rangle)) \\
\equiv \texttt{fstpar}_T(\emptyset, \rho \upharpoonright \{ (\kappa, \kappa) \mapsto \text{Val} \}) \kappa N
\]

Pictorially, we go from

\[
\texttt{fstep} \Rightarrow \kappa :: \text{Par}(0) \\
(1 : \kappa :: s_1 \quad \ldots (i : \kappa :: s_i \ldots \quad (N : \kappa :: s_N)
\]


to

\[
\kappa :: \text{Par}(N) \\
\texttt{fstep} \Rightarrow (1 : \kappa :: s_1 \quad \ldots \texttt{fstep} \Rightarrow (i : \kappa :: s_i \ldots \quad \texttt{fstep} \Rightarrow (N : \kappa :: s_N)
\]

Note that \texttt{fstep} should never be applied to a \textsc{Par} with outstanding threads.
This denotes a serious failure of the semantics, as any given Handel-C statement
only has one live instance at any point in time. In effect we have a pre-condition:

\[
\text{pref}_\text{fstep}_T(\rho)(\kappa \quad \text{where } T(\kappa) = (\text{Par}, \langle s_1, \ldots, s_N \rangle)) \\
\equiv k \in \text{dom } \rho \Rightarrow \rho(k) = \text{Val} \ 0 \quad \text{where } k = (\kappa, \kappa)
\]

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The next execution point of a child of a parallel statement depends on whether or not any other threads remain executing. The parallel construct’s thread count is decremented. If there are still outstanding threads we simply discard the execution point, and return the modified environment. If that was the last thread, then we seek the next statement after the parallel construct:

\[
\text{next}_T(p)(i : \kappa) \quad \text{where} \quad T(\kappa) = (\text{par}, \langle s_1, \ldots, s_N \rangle)
\]

\[\triangleq \quad \text{let } \rho' = \rho \odot \{k \mapsto 1\} \text{ in}
\]

\[\rho'(k) > 0 \rightarrow (\emptyset, \rho')
\]

\[\rho'(k) = 0 \rightarrow \text{next}_T(\rho')\kappa
\]

where \(k = (\kappa, \_t)\)

When there are other threads remaining we go from

\[
\kappa :: \text{par}(t + 1)
\]

\[
(1 : \kappa) :: s_1 \ldots \quad \text{next} \Rightarrow (i : \kappa) :: s_i \ldots (N : \kappa) :: s_N
\]

to

\[
\kappa :: \text{par}(t)
\]

\[
(1 : \kappa) :: s_1 \ldots \quad (i : \kappa) :: s_i \ldots (N : \kappa) :: s_N
\]

When there are no other threads remaining we go from

\[
\kappa :: \text{par}(1)
\]

\[
(1 : \kappa) :: s_1 \ldots \quad \text{next} \Rightarrow (i : \kappa) :: s_i \ldots (N : \kappa) :: s_N
\]

to

\[
\text{next} \Rightarrow \kappa :: \text{par}(0)
\]

\[
(1 : \kappa) :: s_1 \ldots \quad (i : \kappa) :: s_i \ldots (N : \kappa) :: s_N
\]

4.5.5 The Brk Statement

The Break statement simply jumps to the first statement after the most immediately enclosing while-loop, or pri-alt construct. In our reduced abstract syntax, we don’t have explicit breaks in pri-alt, so we view them here as only occurring inside while-loops. While technically an atomic statement, it is treated
here as composite one. It is called by fstep, but not by nxtep:
\[
fstep_T(\rho) \kappa \quad \text{where} \quad T(\kappa) = (\text{BRK}, \Lambda)
\]
\[
\cong nxtep_T(\rho)(\text{brk} \kappa)
\]
\[
\text{where}
\]
\[
\text{brk} : \quad TC \to TC
\]
\[
\text{brk} \Lambda \cong \Lambda
\]
\[
\text{brk}(i : \kappa) \cong T(i : \kappa) = (\text{WHL} \ldots) \to nxtep_T(\rho)\kappa
\]
\[
\to \text{brk} \kappa
\]

Pictorially, we go from
\[
\begin{array}{c}
\kappa :: \text{WHL} \\
\downarrow \\
\vdots \\
\downarrow \\
fstep \Rightarrow j :: -(i : \kappa) :: \text{BRK}
\end{array}
\]

(no WHL here)

\[
\begin{array}{c}
\text{nxtep} \Rightarrow \kappa :: \text{WHL} \\
\downarrow \\
\vdots \\
\downarrow \\
\downarrow \\
j :: -(i : \kappa) :: \text{BRK}
\end{array}
\]

(no WHL here)

4.5.6 The Pri Statement

The effect of fstep on the prialt has already been covered. The next statement
for a prialt sub-statement is simply the next statement for the prialt itself:
\[
xtep_T(\rho)(i : \kappa) \quad \text{where} \quad T(\kappa) = (\text{PRI}, \langle c_1, \ldots, c_N \rangle)
\]
\[
\cong nxtep_T(\rho)\kappa
\]

Pictorially, we go from
\[
\begin{array}{c}
\kappa :: \text{PRI} \\
\downarrow \\
(1 : \kappa) :: c_1 \ldots \\
\downarrow \\
xtep \Rightarrow (i : \kappa) :: c_i \ldots \ldots (N : \kappa) :: c_N
\end{array}
\]

to
\[
\begin{array}{c}
xtep \Rightarrow \kappa :: \text{PRI} \\
\downarrow \\
(1 : \kappa) :: c_1 \ldots \\
\downarrow \\
i :: c_i \ldots \ldots (N : \kappa) :: c_N
\end{array}
\]

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4.6 Definition of resep

The resep function, handles the prialt and communication constructs. Unlike, the other functions, it is given a set of execution pointers denoting all the currently “live” prialts and input and output statements.

\[
\text{resep} : \text{Stmt} \rightarrow \text{Env} \rightarrow \mathcal{P} TC \rightarrow \mathcal{P} State \\
\text{pre-resep}_T(\rho)K \triangleq \forall \text{isComm}_T K \\
isComm : \text{Stmt} \rightarrow \text{TC} \rightarrow \mathbb{B} \\
isComm_T(\kappa) \triangleq \text{case } T(\kappa) \\
(\text{IN} \_A) \rightarrow \text{TRUE} \\
(\text{OUT} \_A) \rightarrow \text{TRUE} \\
(\text{Pri} \_\_A) \rightarrow \text{TRUE} \\
s \rightarrow \text{FALSE}
\]

It determines which cases in these will become active, if any. A prialt without a default clause can itself be blocked, in the same way as is possible for the communications statements. If a prialt is blocked we return a pointer to the whole construct. When a prialt alternative is selected, the relevant communications statement is executed. The associated statements are executed in subsequent clock cycles.

4.6.1 Definition of resep, without prialts

We now give a definition of resep that does not cater for Pri. This means in effect that it changes nothing, as the only statements we expect to handle are atomic communication statements which are already handled.

\[
\text{resep} : \text{Stmt} \rightarrow \text{Env} \rightarrow \mathcal{P} TC \rightarrow \mathcal{P} State \\
\text{resep}_T(\rho)K \triangleq (K, \rho)
\]

4.7 Full Definitions of fstep and nstep

The definitions of fstep and nstep are distributed throughout the preceding text. Here we gather them together in one place.
4.7.1 Complete Definition of \( \text{fstep} \)

\[
\text{pre} \_ \text{fstep}_T(\rho) \kappa \\
\equiv \quad \kappa \in \text{dom } T \\
\quad \land \\
\text{case } T(\kappa) \\
\quad (\text{PAR},\bot) \rightarrow k \in \text{dom } \rho \land \rho(k) = \text{VAL } 0 \\
\quad \text{where } k = (\kappa, \bot) \\
\text{otherwise } \rightarrow \text{TRUE}
\]

\[
\text{fstep}_T(\rho) \kappa \\
\equiv \quad \text{case } T(\kappa) \\
\quad (\text{SEQ}, \bot) \rightarrow \text{fstep}_T(\rho)(1: \kappa) \\
\quad (\text{PAR}, \sigma) \rightarrow \text{fstpar}_T(\emptyset, \rho \uparrow \{ (\kappa, \bot) \mapsto \text{VAL } n \}) \kappa n \\
\quad \text{where } n = \text{len } \sigma \\
\quad (\text{ASG } \bot \Lambda) \rightarrow \{ \kappa \}, \rho \\
\quad (\text{IN } \bot \Lambda) \rightarrow \{ \kappa \}, \rho \\
\quad (\text{OUT } \bot \Lambda) \rightarrow \{ \kappa \}, \rho \\
\quad (\text{If } c, \{ s_l, s_r \}) \mid \rho(c) \rightarrow \text{fstep}_T(\rho)(1: \kappa) \\
\quad (\text{If } c, \{ s_l, s_r \}) \mid \neg \rho(c) \rightarrow \text{fstep}_T(\rho)(2: \kappa) \\
\quad (\text{If } c, \{ s_l \}) \mid \rho(c) \rightarrow \text{fstep}_T(\rho)(1: \kappa) \\
\quad (\text{If } c, \{ s_l \}) \mid \neg \rho(c) \rightarrow \text{nmtep}_T(\rho) \kappa \\
\quad (\text{WHL } c, \bot) \mid \rho(c) \rightarrow \text{fstep}_T(\rho)(1: \kappa) \\
\quad (\text{WHL } c, \bot) \mid \neg \rho(c) \rightarrow \text{nmtep}_T(\rho) \kappa \\
\quad (\text{PRI}, \bot) \rightarrow \{ \kappa \}, \rho \uparrow \{ (\kappa, \bot) \mapsto \text{STOP} \} \\
\quad (\text{BRK}, \Lambda) \rightarrow \text{nmtep}_T(\rho)(\text{brk } \kappa) \\
\quad (\text{DLY}, \Lambda) \rightarrow \{ \kappa \}, \rho
\]

\[
\text{fstpar} : \ 	ext{Stmt}T \rightarrow \text{PState} \rightarrow \text{TC} \rightarrow \text{N} \rightarrow \text{P}[\text{TC} \times \text{Env}] \\
\text{fstpar}_T(P, \rho) \kappa 0 \equiv (P, \rho) \\
\text{fstpar}_T(P, \rho) \kappa (i + 1) \equiv \textbf{let } (P', \rho') = \text{fstep}_T(\rho)(i + 1: \kappa) \\
\textbf{in} \text{fstpar}_T(P \cup P', \rho') \kappa i
\]

\[
\text{brk} : \quad \text{TC} \rightarrow \text{TC} \\
\text{brk } \Lambda \equiv \Lambda \\
\text{brk}(i : \kappa) \equiv \text{fstpar}_T(\rho)(i : \kappa) \\
\rightarrow \text{brk } \kappa
\]
4.7.2 Complete Definition of nstep

\[ \text{nstep}_T(\rho)(i: \kappa) \]

\[ \triangleq \begin{cases} \text{case } T(i: \kappa) \\
\begin{array}{l}
(\text{In } c \ v, \Lambda) \ | \ \text{stateOf}(\rho(i: \kappa, c)) = \text{INPUT} \rightarrow (\{i: \kappa\}, \rho) \\
(\text{Out } c \ e, \Lambda) \ | \ \text{stateOf}(\rho(i: \kappa, c)) = \text{OUTPUT} \rightarrow (\{i: \kappa\}, \rho) \\
(\text{Pri}, \sigma) \ | \ \rho(i: \kappa, \omega) = \text{STOP} \rightarrow (\{i: \kappa\}, \rho)
\end{array} \\
\text{otherwise}
\end{cases} \]

\[ \text{case } T(\kappa) \]

\[ \begin{array}{l}
(\text{Seq}, \sigma) \ | \ i < \text{len } \sigma \rightarrow \text{fstep}_T(\rho)(i + 1: \kappa) \\
(\text{Seq}, \sigma) \ | \ i = \text{len } \sigma \rightarrow \text{nstep}_T(\rho)\kappa \\
(\text{If } \omega, \sigma) \rightarrow \text{nstep}_T(\rho)\kappa \\
(\text{While } c, \omega) \ | \ \rho(c) \rightarrow \text{fstep}_T(\rho)(1: \kappa) \\
(\text{While } c, \omega) \ | \ \neg \rho(c) \rightarrow \text{nstep}_T(\rho)\kappa \\
(\text{Par}, \langle s_1, \ldots, s_N \rangle) \ | \ \rho'(k) > 0 \rightarrow (\emptyset, \rho') \\
\hspace{1cm} \text{where } k = (\kappa, \omega) \text{ and } \rho' = \rho \circ \{k \mapsto 1\} \\
(\text{Par}, \langle s_1, \ldots, s_N \rangle) \ | \ \rho'(k) = 0 \rightarrow \text{nstep}_T(\rho')\kappa \\
\hspace{1cm} \text{where } k = (\kappa, \omega) \text{ and } \rho' = \rho \circ \{k \mapsto 1\}
\end{array} \]

4.8 Auxiliary Functions

We set channel state to waiting for output when we have a input statement using an idle channel:

\[ \text{setOutWait : } TC \rightarrow \text{ValState} \rightarrow \text{ValState} \]

\[ \text{setOutWait}[\kappa_i](\text{CHAN IDLE } \Lambda \ d) \triangleq (\text{CHAN OUTWAIT } \kappa_i \ \Lambda \ d) \]

\[ \text{setOutWait}[\kappa_i](\text{OUTCh IDLE } \Lambda \ d) \triangleq (\text{OUTCh OUTWAIT } \kappa_i \ d) \]

We set channel state to waiting for input when we have a output statement using an idle channel:

\[ \text{setInWait : } TC \rightarrow \text{ValState} \rightarrow \text{ValState} \]

\[ \text{setInWait}[\kappa_o](\text{CHAN IDLE } \Lambda \ d) \triangleq (\text{CHAN INWAIT } \kappa_o \ \Lambda \ d) \]

\[ \text{setInWait}[\kappa_o](\text{OUTCh IDLE } \Lambda \ d) \triangleq (\text{OUTCh INWAIT } \kappa_o \ d) \]

We set channel state to ready when we have an input statement using a channel waiting for input:

\[ \text{setInReady : } TC \rightarrow \text{ValState} \rightarrow \text{ValState} \]

\[ \text{setInReady}[\kappa_i](\text{CHAN INWAIT } \Lambda \ \kappa_o \ d) \triangleq (\text{CHAN READY } \kappa_i \ \kappa_o \ d) \]

We also set channel state to ready when we have an output statement using a channel waiting for output:

\[ \text{setOutReady : } TC \rightarrow \text{ValState} \rightarrow \text{ValState} \]

\[ \text{setOutReady}[\kappa_o](\text{CHAN OUTWAIT } \kappa_i \ \Lambda \ d) \triangleq (\text{CHAN READY } \kappa_i \ \kappa_o \ d) \]

\[ 30 \]
We also set channel state to ready when we have both input and output statements using a channel waiting for output:

\[
\text{setReady} 
\] 
\[
\text{setReady}[\kappa_i, \kappa_o](\text{Chan OutWait} \land d) 
\] 
\[
\equiv \quad \text{(Chan Ready} \, \kappa_i \, \kappa_o, \, d) 
\]

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References


